

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Risho KOH, et al.
Title: FIELD EFFECT TRANSISTOR AND METHOD FOR PRODUCING
THE SAME
Appl. No.: Unassigned
International Filing Date: 01/28/2005
371(c) Date: 07/28/2006
Examiner: Unassigned
Art Unit: Unassigned

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of documents known to Applicants in order to comply with Applicants' duty of disclosure pursuant to 37 CFR §1.56.

A copy of each non-U.S. patent document and each non-patent document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicants do not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the date of entry of the national stage as set forth in 37 CFR §1.491.

RELEVANCE OF EACH DOCUMENT

Document A1 is a U.S. counterpart of Document A4.

Document A2 relates to a double-gate FET device and manufacturing method of the same, capable of solving floating body effect and improving characteristics of the device and which provides a manufacturing method of the same.

Document A3 relates to a semiconductor device which can improve element characteristics.

Document A5 relates to a field effect transistor and its manufacturing method providing a sidewall of an insulation film on a gate electrode.

Document A6 relates to a field effect transistor and manufacturing method thereof to easily apply a vertical field effect transistor to an LSI by providing a structure in common and forming process of a source/drain region and a gate electrode.

The relevance of Document A8 is described in the present application.

Documents A4 and A7 listed on the attached PTO/SB/08 were cited as being relevant during the prosecution of the corresponding International application. A copy of the International Search Report is attached setting forth the portion of each document considered relevant by the examiner. An English translation of each of the foreign-language documents is not readily available. However, the absence of such translation does not relieve the PTO from its duty to consider the submitted foreign language document (37 CFR §1.98 and MPEP §609). English language abstracts are attached.

Applicants respectfully request that each listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

Although Applicant believes that no fee is required for this Request, the Commissioner is hereby authorized to charge any additional fees which may be required for this Request to Deposit Account No. 19-0741.

Respectfully submitted,

Date: July 28, 2006

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10/587845

MODIFIED PTO/SB/08 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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IAPE 28 JUL 2006
Complete if known

Substitute for form 1449B/PTO				Application Number		Unassigned	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date		01/28/2005	
				First Named Inventor		Risho KOH	
Date Submitted: July 28, 2006				Group Art Unit		Unassigned	
(use as many sheets as necessary)				Examiner Name		Unassigned	
Sheet	1	of	1	Attorney Docket Number		040373-0396	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	A1	6,525,403	B2	INABA et al.	02-25-2003	

UNPUBLISHED U.S. PATENT APPLICATION DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Application Document		Name of Patentee or Applicant of Cited Document	Filing Date of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Serial Number	Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office ³	Number ⁴	Kind Code ⁵ (if known)			
	A2	JP	2003-298051	A	LEE SOKO	10-17-2003	A
	A3	JP	2003-124463	A	TOSHIBA CORP.	04-25-2003	A
	A4	JP	2002-110963		TOSHIBA CORP.	04-12-2002	A
	A5	JP	2002-009289	A	NEC CORP.	01-11-2002	A
	A6	JP	2001-298194		NEC CORP.	10-26-2001	A
	A7	JP	06-302817		KAWASAKI STEEL CORP.	10-28-1994	A
	A8	JP	06-302816		KAWASAKI STEEL CORP.	10-28-1994	A

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.		

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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